Project Code No (of Institute): R&D/ANRF/4211/ECE/25-26/684

Applications are invited from Indian nationals for the following position for the research project titled "**SecureLoc: Secure Localization for Future Wireless Communications**" sponsored by the **Anusandhan National Research Foundation** (ANRF).

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1. **Name of the position:** Junior Research Fellow (JRF)
2. **Number of positions: 01**
3. **Last date of Application:** Applicants should apply within **21 days of this advertisement**.
4. **Emoluments (p.m.):** Rs 37,000/- per month
5. **HRA: as per norms**
6. **Essential Educational Qualification:**

If the candidate has M.TECH. degree:

i. M.Tech. with First class in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.
ii. B.Tech with First class in **Electronics and Communication Engineering**
iii. GATE Qualified

If the candidate has B.TECH. degree only:

i. B.Tech with a minimum of 75% in **Electronics and Communication Engineering**
ii. GATE Qualified

7. **Desirable qualification:**

Communication system/MATLAB/FPGA implementation

8. **Age and Age Relaxation**: As per Annexure IV

Application Procedure:

Make a single PDF file having the filled application form (attached) with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate and others. Mail the PDF file to the following email ID "icclab.iitbhu@gmail.com" and CC to "muralikrishnan.ece@iitbhu.ac.in "with the subject line "**SecureLoc: Secure Localization for Future Wireless Communications** ".

Note: No hard copy is required. Applicant needs to show all the original documents at the time of the interview, if shortlisted, based on the submitted data in the application.

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit based on qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately if selected.
5. No TA/DA will be paid for attending the interview.
6. Other relevant Guidelines of the funding agency and the GoI rules will be followed.

Please feel free to contact me in case of any query:

Dr. Muralikrishnan Srinivasan (Principal Investigator),
Assistant Professor,
Department of Electronics Engineering,
Indian Institute of Technology (BHU)
Varanasi 221005 (U.P.) INDIA

Email ID: muralikrishnan.ece@iitbhu.ac.in

Application Form

Personal details:

Name	
Father's name	
Mother's name	
E-mail	
Phone No.	
Date of Birth and Category (GN/OBC/SC/ST/)	**(certificate to be attached if OBC/SC/ST)**
Permanent Address	
Correspondence Address	

Details of Education Qualification

Qualification	University/Board	Year of passing	Percentage /CGPA	Subject
10th				
12th				
B.Tech./B.E.				
M. Tech.				
Others				

Details of qualifying examination (GATE, Net, or Others)

Name of Examination	Subject of Examination	Year of Passing	Score	Marks Obtained

Details of Professional / Work Experience: At least current employment should be mentioned

Organization	Designation	Date-From	Date-To

The details furnished in this form are correct to my knowledge and belief.

Signature with Date and Name